

IN THE CLAIMS

Please amend the Claims as follows:

1 (Currently Amended). A semiconductor stacking structure comprising:

a first semiconductor device; and

a flexible tape substrate having metal layers for electrical connections wherein the flexible tape substrate is coupled to a bottom surface of the first semiconductor device via the metal layers, ~~wherein the flexible substrate is~~ being folded over on at least two sides ~~so as to not overlap and~~ to form flap portions which are coupled to an upper surface of the first semiconductor device, ~~and wherein the flap portions~~ not over lapping one another and covering only a portion ~~section~~ of the upper surface of the first semiconductor device which is smaller than the upper surface of the first semiconductor device, the flexible substrate used for stacking additional semiconductor devices on the flap portions wherein the additional semiconductor devices are coupled to the metal layers of the flexible substrate; and ~~first semiconductor device.~~

2 (Original). A semiconductor stacking structure in accordance with Claim 1 further comprising an adhesive layer which is placed on the flap portions of the flexible substrate and which couples the flap portions to the first semiconductor device.

3 (Original). A semiconductor package in accordance with Claim 1 further comprising an adhesive layer which is placed on the upper surface of the first semiconductor device and which couples the flap portions to the first semiconductor device.

4 (Original). A semiconductor stacking structure in accordance with Claim 1 further comprising a second semiconductor device coupled to the flap portions of the flexible substrate.

5 (Original). A semiconductor stacking structure in accordance with Claim 4 wherein the second semiconductor device is coupled to the flap portions of the flexible substrate after the flap portions are folded over and coupled to the first semiconductor device.

6 (Original). A semiconductor stacking structure in accordance with Claim 4 wherein the second semiconductor device is coupled to the flap portions of the flexible substrate before the flap portions are folded over and coupled to the first semiconductor device.

7 (Original). A semiconductor stacking structure package in accordance with Claim 1 wherein the semiconductor stacking structure is a LGA (Land Grid Array) device.

8 (Original). A semiconductor stacking structure in accordance with Claim 1 wherein the semiconductor stacking structure is a BGA (Ball Grid Array) device.

9 (Original). A semiconductor stacking structure in accordance with Claim 1 wherein the semiconductor stacking structure is a lead type of device.

10 (Original). A semiconductor stacking structure in accordance with Claim 1 wherein the flexible substrate is folded over on four sides to form flap portions which are coupled to the upper surface of the first semiconductor device and covers only a portion of the upper surface of the first semiconductor device.

11 (Currently Amended). A semiconductor stacking structure comprising:

a first semiconductor device; and

means having metal layers for electrical connections coupled to a bottom surface of the first semiconductor device via the metal layers for forming the semiconductor stacking structure, wherein the means is folded over on at least two sides so as to not overlap and coupled to an upper surface of the first semiconductor device and covers only a portion of the upper surface of the first semiconductor device which is smaller than the upper surface of the first semiconductor device, the flexible substrate used for stacking additional semiconductor devices on folded over portions of the means, the additional semiconductor devices coupled to the metal layers of the means ~~the first semiconductor device~~.

12 (Original). A semiconductor stacking structure in accordance with Claim 11 further comprising an adhesive layer placed on the means for coupling the means to the upper surface of the first semiconductor device.

13 (Examiner Withdrawals From Consideration).

14 (Examiner Withdrawals From Consideration).

15 (Examiner Withdrawals From Consideration).

16 (Examiner Withdrawals From Consideration).

17 (Examiner Withdrawals From Consideration).

18 (Examiner Withdrawals From Consideration).

19 (Examiner Withdrawals From Consideration).

20 (Examiner Withdrawals From Consideration).

21 (Currently Amended). A semiconductor stacking structure comprising:

a first semiconductor device; and

~~means~~ a flexible tape substrate having metal layers on a top and bottom surface for electrical connections, the flexible tape substrate coupled to a bottom surface of the first semiconductor device via the metal layers for forming the semiconductor stacking structure wherein the means is folded over on ~~at least two~~ four sides so as to not overlap to form flap portions which are coupled to an upper surface of the first semiconductor device and covers only a portion of the upper surface of the first semiconductor device which is smaller than the upper surface of the first semiconductor device, the flap portions increasing connect density of the semiconductor stacking structure and used for stacking additional semiconductor devices on the first semiconductor, the additional semiconductor devices coupled to the metal layers of the means.

22 (Original). A semiconductor stacking structure in accordance with Claim 21 further comprising means placed on the flap portions of the flexible substrate for coupling the flap portions to the first semiconductor device.

23 (Original). A semiconductor package in accordance with Claim 21 further comprising means placed on the upper surface of the first semiconductor device for coupling the flap portions to the first semiconductor device.

24 (Original). A semiconductor stacking structure in accordance with Claim 21 further comprising a second semiconductor device coupled to the flap portions of the ~~means~~ flexible tape substrate.

25 (Original). A semiconductor stacking structure in accordance with Claim 24 wherein the second semiconductor device is coupled to the flap portions of the ~~means~~ flexible tape substrate after the flap portions are folded over and coupled to the first semiconductor device.

26 (Original). A semiconductor stacking structure in accordance with Claim 24 wherein the second semiconductor device is coupled to the flap portions of the ~~means~~ flexible tape substrate before the flap portions are folded over and coupled to the first semiconductor device.

27 (Original). A semiconductor stacking structure package in accordance with Claim 21 wherein the semiconductor stacking structure is a LGA (Land Grid Array) device.

28 (Original). A semiconductor stacking structure in accordance with Claim 21 wherein the semiconductor stacking structure is a BGA (Ball Grid Array) device.